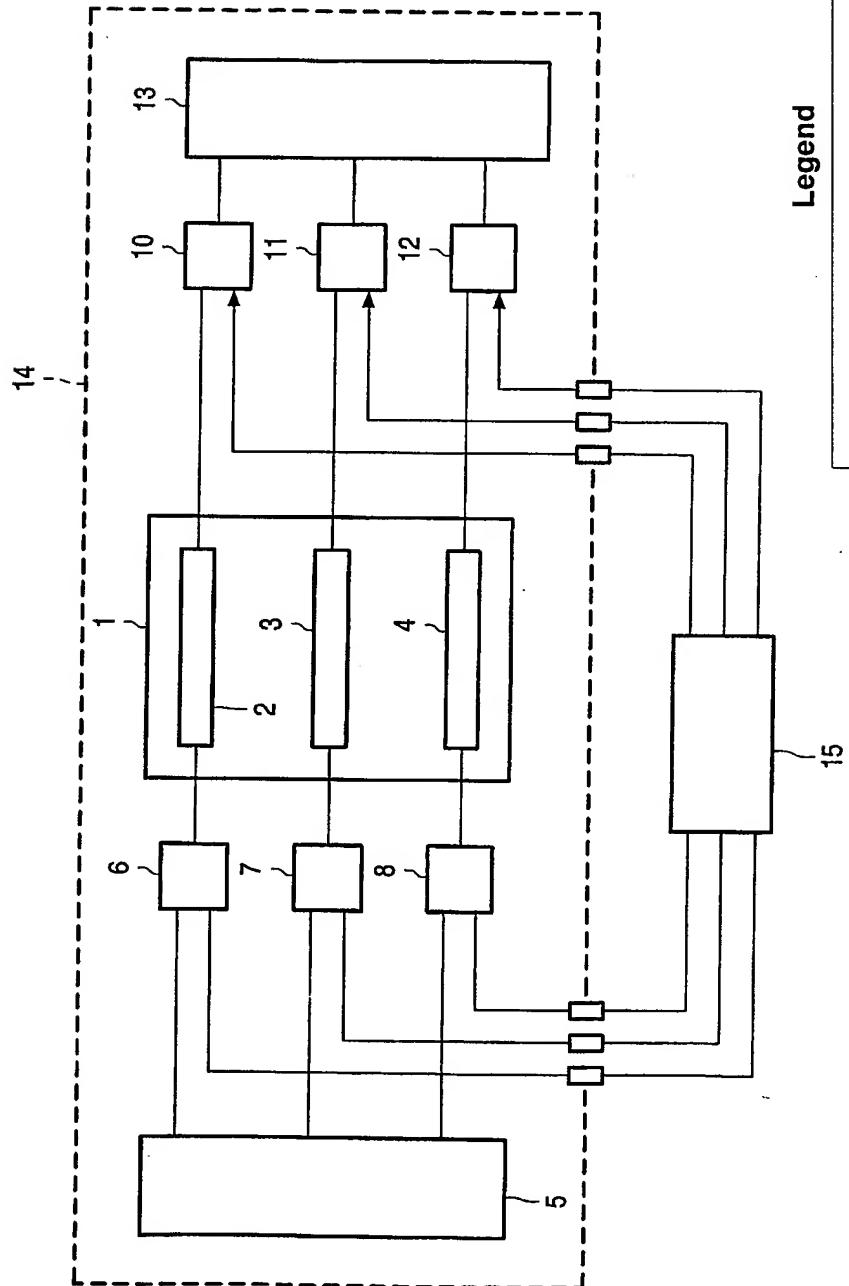




1/1



Legend

| | |
|------------|--------------------------------|
| 1 | Application Circuit |
| 2, 3, 4 | Shift Registers |
| 5 | Linear Feedback Shift Register |
| 6, 7, 8 | First Logic Gates |
| 10, 11, 12 | Second Logic Gates |
| 13 | Signature Register |
| 14 | Integrated Circuit |
| 15 | Test Setup |